

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application.
Please cancel claims 1-10 and add claims 11-20 as shown in the following listing of claims.

1-10. (cancelled)

11. (new) A method of controlling the voltage swing in a circuit for transferring data to a data bus, comprising:

- (a) coupling a pass transistor with the bus;
- (b) coupling a discharge transistor between the pass transistor and ground;
- (c) coupling an inverter between the discharge transistor and the pass transistor, the inverter and the discharge transistor forming a signal node; and
- (d) selectably driving the pass transistor gate with the inverter in response to a signal node voltage value.

12. (new) The method of claim 11, further comprising programming the discharge transistor to impose a preselected bus operational characteristic on the data bus.

13. (new) The method of claim 11, further comprising programming the pass transistor to impose a preselected bus operational characteristic on the data bus.

14. (new) The method of claim 11, wherein coupling operation (b) comprises coupling a plurality of discharge transistors between the pass transistor and ground.

15. (new) The method of claim 14, further comprising selectively programming ones of the plurality of discharge transistors to impose respective preselected bus operational characteristic on the data bus.

16. (new) The method of claim 11, wherein the pass transistor couples the data bus with one of a global sense amplifier, a local sense amplifier, a global wordline decoder, and a local wordline decoder.

17. (new) The method of claim 13, wherein the pass transistor couples the data bus with one of a global sense amplifier, a local sense amplifier, a global wordline decoder, and a local wordline decoder.

18. (new) A method of controlling voltage swing in a data bus transfer circuit for transferring data between a first data bus and a second data bus, comprising:

- (a) coupling a first pass transistor between the first data bus and a first inverter, the coupling of the first pass transistor and the first inverter forming a first signal node;
- (b) coupling a second pass transistor between the second data bus and a second inverter, the coupling of the second pass transistor and the second inverter forming a second signal node;
- (c) coupling a first signal discharge transistor between the first signal node and ground; and
- (d) coupling a second signal discharge transistor between the first signal node and ground, wherein the first inverter transfers a signal representative of first data on the first data bus to the second signal discharge transistor and the second inverter transfers a signal representative of second data on the second data bus to the first signal discharge transistor.

19. (new) The method of claim 18, further comprising cross-linking the second inverter to the first inverter.

20. (new) The method of claim 18, further comprising:

- (e) coupling a first node charge transistor between V_{dd} and the first signal node;
- (f) coupling a first node discharge transistor between the first signal discharge transistor and ground;
- (g) coupling a second charge transistor between the V_{dd} and the second signal node;
- (h) coupling a second node discharge transistor between the second signal discharge transistor and ground, wherein each of the first and second node charge transistors pulls the respective first and second signal nodes to V_{dd} when a LOW gate signal is applied thereto.

21. (new) The method of claim 20, further comprising operably coupling a clocking signal with the first node charge transistor, the first node discharge transistor, the second node charge transistor, and the second node discharge transistor.

22. (new) The method of claim 18, further comprising coupling one of the first data bus and the second data bus with one of a global sense amplifier, a local sense amplifier, a global wordline decoder, and a local wordline decoder.

23. (new) The method of claim 21, further comprising coupling one of the first data bus and the second data bus with one of a global sense amplifier, a local sense amplifier, a global wordline decoder, and a local wordline decoder.